

Docket No. 1232-4421US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)

UEHARA et al.

Serial No.

09/664,715

Art Unit

1765

Filed

September 19, 2000

Examiner

K. Chen

For

WAFER PROCESSING APPARATUS, WAFER PROCESSING

METHOD, AND SEMICONDUCTOR SUBSTRATE

FABRICATION METHOD

RESPONSE

MAIL STOP - Non-Fee Amendment COMMISSIONER FOR PATENTS

P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Kindly consider this Response to the Office Action dated April 25, 2003, in the above-identified application.

Remarks begin on Page 2 of this paper.